Iterative Macro-Operators Revisited: Applying Program Synthesis to Learning in Planning

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Keywords: macro generation, planning, inductive program synthesis

Abstract

The goal of this paper is to demonstrate that a method for inductive program synthesis (as described in [SW98]) can be applied to the problem of learning cyclic (iterative/recursive) macro-operations from planning. Input in the program synthesis system is a so-called initial program which represents an ordered set of straight-forward transformations from input states to the desired output. In the context of planning, the input states correspond to initial states, the output state to the planning goal, and transformations are shortest operation sequences. Ordering of transformations can be achieved by calculating a minimal spanning tree for the problem graph with the state(s) fulfilling the goal as root. We have implemented a non-linear backward planner which generates such a complete partial order as a by-product of planning. Output of the program synthesis system is a recursive program scheme representing the generalization of a program limited to solving a finite problem of given size to a general solution strategy. Our synthesis method is embedded in the theory of the semantic of functional programs and in the theory of inductive inference (see [MS98]) and thereby provides a sound formal basis for macro-construction. The current implementation can generalize tail, linear and tree recursive structures and combinations of such structures with multiple (and possibly interdependent) recursive parameters.

Contents

Chapter 1

Introduction

Learning macro-operators is a topic of interest nearly since the beginning of planning research [FN71a, Min85, Kor85]. While macros do not lead in general to greater efficiency in planning [Min85, DC96], they are nevertheless a useful and interesting contribution to planning research. Firstly, if construction of macros is not performed blindly but augmented by information about occurrence frequencies of operator sequences and if retrieval of macros is organized efficiently, planning can be speed-up considerably when introducing linear macros [Min85]. A linear macro-operator generally represents an aggregation of the pre- and post-conditions over a sequence of primitive operators. Using a macro-operator instead of a set of primitive operators reduces the number of match-select-apply cycles executed during planning and thereby also the number of planning decisions which might result in backtracking. For iterative macros efficiency gains should be even higher, because they not only provide aggregated operators but additionally incorporate part of the control strategy for planning [SC89, BV96].

Secondly, learning in planning is per se a topic of interest for cognitive science and artificial intelligence. Work on linear macro-operators in planning has its parallel in work on learning by chunking [RN86] and knowledge compilation [And86] in cognitive science. While this work addresses the fact that human problem solvers improve their skills by experience (i.e. faster generation of solutions which less errors in accordance with the power law of practice [NR81]), it neglects a second important aspect of human learning: When solving a problem of given complexity (as the Tower of Hanoi with three discs) humans can extrapolate a general strategy for solving problems of the same type with arbitrary complexity (as Tower of Hanoi problems with an arbitrary number of discs; [Kla78]). In production system architectures with fixed interpreter strategy this kind of learning cannot be modelled [SC89, SWar]. Therefore, coming up with a technique for learning cyclic macro-operators from experience is a challenging problem for planning as well as cognitive science research.[1]

[1]Now macro learning becomes also of interest in reinforcement learning. See NIPS'98

The importance of learning iterative macro-operators was first addressed by Carbonell, Cheng and Shell [CC86, SC89, DC96] and is also acknowledged by Schmid in the context of machine learning [SW98] and cognitive science [SWar]. While the work of Carbonell and colleagues addresses efficiency gains in planning and gives ideas about a technique for constructing iterative macros in context of the expert system FERMI, Schmid proposes to combine planning and program synthesis in the following way: Planning provides straight-forward "programs" for transforming input into desired output states. These straight-forward programs can be generalized by a technique for inductive program synthesis. The first step corresponds to exploring a problem or – in the context of program construction – to hand-simulating I/O transformations. The second step corresponds to (unsupervised) learning from experience and – in the context of automatic programming – to programming by demonstration [Coh98, Wel99a]. While the first step is dependent on background-knowledge (especially on the semantics of predefined operations and on knowledge about data structures), the second step can be performed (with some limitations, see [MS98]) purely syntactically. That is, generation of initial programs is dependent on heuristic information – but the synthesis method corresponds to a generic pattern-matching algorithm which can be completely formalized and its soundness, efficiency and scope can be determined in a general way.

In this paper we will focus on generating initial programs from planning. The synthesis technique is described in detail elsewhere [SW98, MS98]. Because our approach is somewhat out of the context of current research in planning, we start with an informal example to motivate our ideas. More concrete, we will show that the function *clearblock* can be easily inferred from planning experience. The *clearblock*-function is an example of a simple iterative macro [MW87], corresponding to a linear recursion. Afterwards, we will introduce our planning system and discuss it in relation to other planning systems as PRODIGY [VCP$^+$95], UCPOP [PW92], graphplan-approaches [BF97, KNH97] and universal planning [Sch87, Sch95, CRT98]. Our system DPlan is a sound and complete non-linear backward planner implemented in LISP. In the fourth part, we will give further examples of learning linear-recursive macros, as for example a general solution strategy for the rocket-problem with an arbitrary number of objects [VC93b] and afterwards we will discuss more complex examples as constructing a tower of ordered blocks and Tower of Hanoi; furthermore we will address the application of planning to list and number problems. We will conclude with an evaluation of our approach and further work to be done.

workshop "Abstraction and Hierarchy in Reinforcement Learning": Amy McGovern, University of Massachusetts, Amherst, acQuire-macros: An Algorithm for Automatically Learning Macro-Actions, and David Andre, University of California, Berkeley, Learning Hierarchical Behaviors; http://www-anw.cs.umass.edu/~ dprecup/call_for_participation.html.

Chapter 2

How to clear a block – inductive generalization of plans

2.1 A recursive function for clearing a block

A human problem solver in many cases is able to generalize a strategy for solving a complete class of problems from his/her experience of solving some example problems. For example, if a person is able to clear the bottom block of a three block tower, we assume that he/she also is able to clear the bottom block of a tower consisting of an arbitrary number of blocks, even if the person has never performed this action sequence before. The need of automated mechanisms for this kind of learning by experience was for example addressed more than a decade ago by Manna and Waldinger [MW87]. Our proposal differs in two aspects from this seminal work: firstly, while Manna and Waldinger discuss the synthesis of imperative programs, we will discuss the synthesis of functional programs; secondly – and more crucially – Manna and Waldinger proposed a *deductive* technique for synthesizing conditional and recursive plans while we are proposing an *inductive* technique, based on inductive program synthesis.

The plan Manna and Waldinger can derive automatically from a goal specification by their deductive tableau method is

$$makeclear(a) \Leftarrow \begin{cases} \textbf{if} & clear(a) \\ \textbf{then} & \Lambda \\ \textbf{else} & makeclear(hat(a)); \\ & put(hat(a), table). \end{cases}$$

To clear a block a, first determine whether it is already clear. If not, clear the block that is on top of block a and then put that block on the table. If a block a is clear, the empty sequence of operations Λ is returned.

Our functional variant of this program is

$$clearblock(x,s) = g(cleartop(x), s, puttable(topof(x), clearblock(topof(x), s)).$$

We represent conditionals by the McCarthy-conditional $g(x,y,z) \equiv$ *if x then y else z*. To clear a block x in a situation s, check whether x is already clear. If yes, do nothing and return the current situation; otherwise, put the block lying on top of block x on the table in a situation where this block is already cleared. The situation variable s holds a conjunction of propositions, as {*on(A B), on(B C), clearblock(A)*} where A, B and C are constants. The predicate *cleartop(x)* is true, if x is currently instantiated with a constant K and *clearblock(K)* is an element of the current situation s. The function *topof(y)* is defined as *on(x y)* $\equiv$ *topof(y) = x)*. Application of the operator *puttable(y,s)* has the effect that *on(y z)* is deleted and *clearblock(z)* is added from/to the propositions representing situation s. The function *clearblock(x,s)* is linear recursive, that is, it corresponds to a loop or – in other words – to an iterative macro-operation. In contrast to the iterative macros proposed by Shell and Carbonell [SC89], in our case, if the precondition is fulfilled the macro terminates, otherwise, the operation *puttable* is (repeatedly) applied.

Excursion: Inductive synthesis of recursive program schemes

We will present the general idea of our synthesis method in a nutshell. The method was originally proposed by Wysotzki [Wys83] as an extension of Summers' method [Sum77] and was extended further, implemented and formalized in the framework of grammar inference by Schmid [SW98, MS98].

The synthesis technique starts with an initial program as input and tries to fold it by extrapolating a recursive program scheme. An initial program is a nested conditional expression representing an ordering of straight-forward transformations. We can synthesize the *clearblock*-function from the following initial program:

$$\begin{aligned} G \;=\; & g(cleartop(x), s, puttable(topof(x), \\ & \quad g(cleartop(topof(x)), s, puttable(topof(topof(x)), \\ & \qquad \Omega)))). \end{aligned}$$

This initial program represents the experience of clearing a block in a world consisting of maximally three blocks. If there are more blocks (i.e. $topof(topof(x))$ is not clear), the actions are undefined (Ω). Note, that the initial program corresponds to the second unfolding of the *clearblock*-function given above. That is, for program synthesis we reverse the idea of determining the semantic of a recursive function as its smallest fixpoint [FH88]: from a given sequence of unfoldings we want to extrapolate the minimal recursive program scheme which can generate these unfoldings.

An initial program G can be folded into a recursive program iff it can be decomposed into a sequence $\mathcal{G}^{(0)} = \Omega$, $\mathcal{G}^{(l)} = tr(\mathcal{G}^{(l-1)}_{[t/v]}/m)$ with $l = 1 \ldots n$, $\mathcal{G}^{(n)} = G$ of partial transformations which successively cover a larger amount of inputs. That is, we have to find a term tr in G which has the characteristic that successively more complex subterms of G can be expressed by inserting the previous subterm at position m where variables v are substituted by terms t. For our example we have:

$$\begin{aligned}
\mathcal{G}^{(0)} &= \Omega \\
\mathcal{G}^{(1)} &= g(cleartop(x), s, puttable(topof(x), \Omega)) \\
\mathcal{G}^{(2)} &= g(cleartop(x), s, puttable(topof(x), \\
&\quad g(cleartop(topof(x)), s, puttable(topof(topof(x)), \Omega)))) \\
&= G
\end{aligned}$$

with *tr = g(cleartop(x), s, puttable(topof(x), m))* and the substitutions $[topof(x)\ /x]$.

Because $\mathcal{G}^{(l)} = tr(\mathcal{G}^{(l-1)}_{[t/v]}/m)$ holds for all $\mathcal{G}$'s,

$$\begin{aligned}
\mathcal{G}^{(0)} &= \Omega \\
\mathcal{G}^{(1)} &= g(cleartop(x), s, puttable(topof(x), \mathcal{G}^{(0)}_{[topof(x)/x]})) \\
\mathcal{G}^{(2)} &= g(cleartop(x), s, puttable(topof(x), \mathcal{G}^{(1)}_{[topof(x)/x]}))
\end{aligned}$$

we can fold G into $\mathcal{G} = tr(\mathcal{G}_{[t/v]}/m)$. For our example that is

$$\mathcal{G} = g(cleartop(x), s, puttable(topof(x), \mathcal{G}_{[topof(x)/x]}))$$

which corresponds to the *clearblock*-program given above.

Our method is defined independently of a given programming language. Instead, we consider programs as terms which are elements of some arbitrary term algebra and we infer recursive program *schemes* [CN78] which can be interpreted with respect to a programming language. With our method we can infer tail recursive structures (i.e. for-loops), linear recursive structures (i.e. while-loops), tree-recursive structures and combinations thereof. We can deal with programs starting with a constant part and – what is beyond the scope of ILP-approaches [MDR94, FYar] – we can deal with multiple recursive parameters which can be interdependent. What is currently out of the scope of our method are substitutions which are themself performed recursively (as in the *ackerman*-function). For details about the formal background, the synthesis algorithm, its scope and complexity, see [SW98, MS98].

Our method of program synthesis from initial programs can be seen as an approach to learning by generalizing over experience or – in the context of programming – as programming by demonstration. From the perspective of planning research, we argue that planning provides initial experience with a problem (i.e. a kind of initial program) which we can generalize by means of program synthesis. That is, we propose a method which makes it possible to infer a large class of cyclic macros in a formally sound way. From the perspective of program synthesis research, we argue that the method to construct initial programs by rewriting input/output examples (see below) can be replaced by more powerful (and more natural) planning techniques. To sum up, we propose the following steps for learning cyclic macro-operators:

1. Constructing initial programs from input/output examples:
   (a) Construct an ordered set of straight-forward transformations from input states to output (i.e. goal) states by planning
   (b) Transform the plan into an initial program
2. Generalizing initial programs into cyclic macro-operations by program synthesis.

Above, we showed, how the cyclic macro for the *clearblock*-problem can be inferred from an initial program. In the rest of this section, we will describe the first step – constructing an initial program by planning – for the *clearblock*-problem.

2.2 A plan for clearing a block in a finite problem space

In the following we will show how the initial program for the *clearblock*-problem can be automatically constructed by planning. For the simple *clearblock*-problem we need only one operator – *puttable* – which we define in a STRIPS-like syntax:

```
(setq rules '( (rule puttable
                 (if ( ct (> x) ) ( on (< x) (> y) ) )
                 (then (puttable (< x)))
                 (conq ( add ((ct (< y)))
                         del ((on (< x) (< y))) ) )
) )).
```

The presented notation is the one used for our planner DPlan: The preconditions are given as a list starting with the keyword *if* (with *ct* as shorthand notation for *cleartop*). The preconditions are interpreted as conjunction of positive literals with existential quantified variables. Variables are represented by $(> x)$ signaling that the binding for x is to be obtained from the current state or $(< x)$ signaling that x has to be instantiated in accordance with the current bindings. The operator is given after the keyword *then*. The operator effect is given as usual by add- and del-lists (which represent conjunctions of positive literals with existential quantified variables). Our planner can also deal with conditional effects which we will show in the next section.

For our example we assume a blocksworld consisting of three blocks A, B and C. Our planning goal is

```
(setq goal '( (ct C) ) ).
```

DPlan can deal with goals which are composed of a conjunction of (possibly interdependent) subgoals which will also be discussed in the next section.

To be able to generalize, we need to make the following experience: clearing C when it is the bottom of a three-block tower, clearing C when it is the bottom of a two-block tower and clearing C when it is already clear. That is, we want to regard for example the following initial states:

```
(setq states '(
   ((on A B) (on B C) (ct A))
   ((on B C) (ct A) (ct B))
   ((ct A) (ct B) (ct C))
)).
```

Typically, a planner would produce a separate plan for each of the initial states. In contrast, we want to deal with all three states in a single planning process and thereby obtain a plan covering all given initial states[1]. The idea, to use planning over sets of states in program synthesis was originally proposed by Wysotzki [Wys87].

Informally, our planner proceeds in the following way:

1. Search for all states in `states` which are subsumed by `goal` (i.e. where the set of goal literals is a subset of a state).
2. If there are no such states: stop with error (the goal cannot be fulfilled) else delete these states from `states` and use them as root for the plan (If there is only one state fulfilling the goal, use this state as root, otherwise introduce a top-node with all states fulfilling the goal as children.).
3. For each node in the plan do recursively until `states` is empty:
   (a) Calculate all immediate predecessors (by backward-application of operator effects). Insert the operations as arc-labels and the predecessors as new nodes in the plan[2].
   (b) Delete all states corresponding to the predecessors from `states`.

Deleting all states corresponding to calculated predecessors from the set of initial states guarantees that only the shortest possible (optimal) operation sequences to transform a state into the goal are calculated and that there occur no cycles. Furthermore, the planner can work completely without backtracking. The resulting plan is a complete partial order over the initial states, i.e. the minimal spanning tree [GH85] of the problem space with the goal state(s) as root. Each planning step is saved as a structure of instantiated operation, predecessor node, constructed new state (child node), and the instantiated preconditions and effects.

To generate (shortest) operation sequences for transforming a set of n initial states into the desired goal, in classical approaches planning has to be performed n times. Each planning episode could involve backtracking. We are arguing, that regarding a set of states "simultaneously" in one backtracking-free planning process is more efficient than planning n times with backtracking. Furthermore,

[1] We will discuss the relation of DPlan to universal planning in the next chapter.
[2] This step will be discussed in detail in the next chapter.

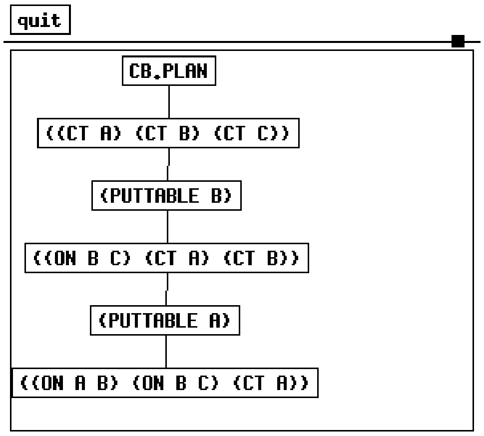

Figure 2.1: Output of DPlan for *clearblock*

by providing the planner with a set of states, there is no need of providing axioms for checking the admissibility of predecessor states. Instead, we can determine if a new state is legal by a look-up in the set of states. Finally, if generating predecessor states involves instantiation of free variables (as y in puttable) the set of legal instantiations can also be calculated by look-ups in the set of states. In section 3 we will present these aspects of DPlan in detail.

The output DPlan generates for the *clearblock*-problem is given in figure 2.1[3]. Only one state fulfilled the goal `(ct C)`. This state is the root of the plan. Only one operation – `(puttable A)` – is applicable. Its backward application results in a single legal predecessor state. Again, only one operation – `(puttable B)` – is applicable. The next predecessor cannot be expanded further. All states from `states` have been used in planning. The plan tree (only a list for this simple example) represents the shortest operator sequences for transforming each initial state into the goal: `((CT A) (CT B) (CT C))` fulfills the goal already, the other states can be transformed by: *puttable(A, ((on B C) (CT B) (CT A)))* and *puttable(B, puttable(A, ((on A B) (on B C) (CT A))))*.

2.3 Replacing constants by constructive expressions

In the rest of this section we will describe how the plan produced by DPlan is transformed into an initial program. The crucial aspect of this transformation is to replace constants by constructive expressions. To motivate this step, we

[3] All graphics are the original outputs produced by DPlan. Operations are regarded as arc-labels but are represented as intermediate nodes between two states in the graphical output of DPlan.

make an excursion to program synthesis again.

An excursion to Summers' method

In Summers' [Sum77] classical approach to inductive program synthesis, the first step is to rewrite the given input/output examples constructively. To use Summers' own example (and notation), if we have the I/O-pairs

$$\begin{array}{rcl} (\,) & \rightarrow & (\,) \\ (A) & \rightarrow & ((A)) \\ (AB) & \rightarrow & ((A)(B)) \\ (ABC) & \rightarrow & ((A)(B)(C)) \end{array}$$

the righthand-sides (outputs) are rewritten into functional expressions for transforming the input into the output

$$\begin{array}{rcl} \{f_1[x] & = & nil; \\ f_2[x] & = & cons[x;nil]; \\ f_3[x] & = & cons[cons[car[x];nil];cons[cdr[x];nil]]; \\ f_4[x] & = & cons[cons[car[x];nil];cons[cons[cadr[x];nil]; \\ & & \quad cons[cddr[x];nil]]]\}. \end{array}$$

Rewriting is performed by applying the predefined functions *car*, *cdr* their legal compositions and *cons* to the input-part of the example in such a way that they transform the input into the desired output[4]. The I/O-examples can now be written as

$$\begin{array}{rcl} \{() & = & nil; \\ (A) & = & cons[x;nil]; \\ (AB) & = & cons[cons[car[x];nil]cons[cdr[x];nil]]; \\ (ABC) & = & cons[cons[car[x];nil]cons[cons[cadr[x];nil] \\ & & \quad cons[cddr[x];nil]]]\}. \end{array}$$

This corresponds to the minimal spanning tree generated by DPlan: for each state (input example) we are provided with the shortest operation sequence to transform the input into the goal (desired output) and these sequences are ordered by their complexity. We will show later, that planning provides a far more powerful method for rewriting I/O-examples than the technique proposed by Summers.

After rewriting the output-parts of the examples, we need a more abstract way to discriminate between the different inputs. The equations above express statements as "if parameter x of function $f(x)$ has value '(A)' then return (*cons x nil*)". Now we want to replace the input values by boolean expressions as "if x is a one-element list". To obtain this,

[4] Summers' method was restricted to functions with a single list as argument which return a list by using only the primitive operations *car*, *cdr* and *cons*. Rewriting was restricted much more than it is the case in genetic programming (see for example [Koz92] for the synthesis of the "tower" program).

Summers relies on a predefined complete partial order (cpo) over lists (with the empty list as bottom-element). He abstracts from the concrete inputs and classifies their abstract forms with respect to the cpo. For example, the form of $(A\ B)$ is $(\omega\ \omega)$ with $(\omega) < (\omega\ \omega) < (\omega\ \omega\ \omega)$ can be characterized by $atom[cddr[x]]$. We omit the details given in [Sum77]; for our example we obtain:

$$
\begin{aligned}
p_1[x] &= atom[x] \\
p_2[x] &= atom[cdr[x]] \\
p_3[x] &= atom[cddr[x]] \\
p_4[x] &= T.
\end{aligned}
$$

The first step of Summers' program synthesis technique is finished when the $p_i \to f_i$ expressions are combined into a single function

$$
\begin{aligned}
F[x] \quad \leftarrow \quad & [atom[x] \to nil \\
& atom[cdr[x]] \to cons[x; nil] \\
& atom[cddr[x]] \to cons[cons[car[x]; nil]; cons[cdr[x]; nil]] \\
& T \to cons[cons[car[x]; nil]; cons[cons[cadr[x]; nil]; \\
& \qquad cons[cddr[x]; nil]]]].
\end{aligned}
$$

This is the kind of initial program, Summers constructs from I/O-examples. In the next step, the initial program is folded into a recursive program in a similar but more restricted way than our method (see section 2.1). By detecting regularities between succeeding subexpressions, the following program for "unpacking" lists is synthesized:

$$
\begin{aligned}
unpack[x] \quad \leftarrow \quad & [atom[x] \to nil; \\
& T \to u[x]] \\
u[x] \quad \leftarrow \quad & [atom[cdr[x]] \to cons[x; nil]; \\
& T \to cons[cons[car[x]; nil]; u[cdr[x]]]].
\end{aligned}
$$

To come back to the *clearblock*-problem: Planning provided us with (nearly) the righthand-side of such straight-forward programs. For our *clearblock* example (cf. figure 2.1) we have:

$$
\begin{aligned}
f_1[x, s] &\to s \\
f_2[x, s] &\to puttable(B, s) \\
f_3[x, s] &\to puttable(A, puttable(B, s)).
\end{aligned}
$$

In contrast to Summers, we still have the constants given in the states instead of more abstract expressions. Summers used knowledge about the structure of lists and a predefined order over their complexity to rewrite inputs as *(A B)* into *atom[cddr[x]]*. We will do something similar to this idea: providing our system with background knowledge about the structure of the problem domain in the form of rewrite-rules. For the *clearblock*-problem we provide the following information

```
; Predicates which indicate constructive rewriting
(defun is-c-pred (p-name)
  (equal p-name 'on)
)

; rewrite rule
; p is a proposition (predicate-name arguments)
; c-eq returns an association list
; of constant - constructive-expression pairs
; (on x y) == [x = (topof y)]
(defun c-eq (p)
  (list (nth 1 p) (list 'topof (nth 2 p))) ; assoc-list
).
```

A proposition $on(K_1, K_2)$ gives us the information that constant K_1 is on the top of constant K_2. So, we use all *on*-predicates in a linear (sub-) plan to generate bindings of constants with their constructive equivalent. This idea corresponds to Manna and Waldinger's use of the *hat*-axiom [MW87]:

$$\begin{array}{l} \textit{if not } Clear(w, y) \\ \textit{then } On(w, hat(w, y), y) \text{ for all states } w \text{ and blocks } y. \end{array}$$

For the *clearblock*-problem we obtain the bindings *((A = topof(topof(C))) (B = topof(C)))* by applying the following (here only informally given) algorithm

- Find substitutions
  - For each proposition p of each state Do
    - If `is-c-pred (predicate-name of` p`)` then return `(c-eq p)`(i.e. the binding of the constant(s) with their constructive equivalent)
    - Union all found bindings
- Apply substitutions
  - For each proposition of each state Do
    - As long as applying the substitutions results in a different form of the arguments of the proposition Do
      replace the arguments by their constructive equivalence.

"Find substitutions" gives us *((A = topof(B)) (B = topof(C)))*; and "Apply substitutions" rewrites all propositions of each state recursively. That is, $topof(B)$ gets rewritten to $topof(topof(C))$. For problems more complex than *clearblock* rewriting is done for each linear subplan (see chapter 3). The output generated by DPlan is given in figure 2.2. Now – as for the righthand sides of Summers' straight-forward programs – the operations are represented in a constructive way and – what we will need for constructing the boolean conditions – the states itself are represented over a constructive data type also. The remaining constant symbols in the plan can now be interpreted as variables (in correspondence to Summers' abstract forms of the original inputs).

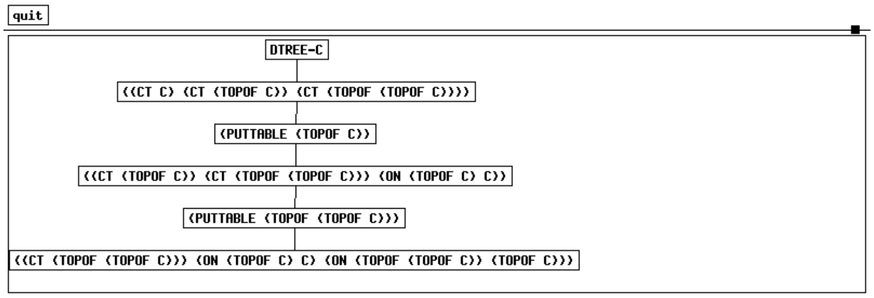

Figure 2.2: Result of constructively rewriting constants for *clearblock*

Using background-knowledge – as Summers' cpo or Manna and Waldinger's axioms – is an important technique for learning cyclic macro-operators in planning. While background knowledge is seldomly regarded in classification learning [Mit97] it is central (domain theory) in explanation based learning [MKKC86] and also common practice in the context of program synthesis from examples (see cf. the inductive logic programming literature [MDR94, FYar] and the genetic programming literature [Koz94])[5].

2.4 Determining relevant predicates

In the next step, will determine the minimal set of relevant predicates of each constructively rewritten state of the plan. This corresponds to Summers' introduction of boolean expressions as the lefthand-sides of his straight-forward programs – and ins some way also to the discrimination predicates used in conditional planning [PS92, BV97]. The relevant predicates can be identified easily from the information obtained during planning: Each legal planning step consists of an instantiated operator which transforms a state (the state newly constructed by backward-planning) into the state given at the current node. That is, the predicate(s) in the add-list of the operator must match with predicate(s) of the current state. These predicates are the one which are fulfilled in the current planning step and therefore, they are the relevant predicates.

For the *clearblock*-example we obtain: *(ct C)* for the root-node which is generated by *puttable(topof C)* and *(ct (topof C))* for the second node which is generated by *puttable(topof(topof C))*. The leafs of a plan represent states for which exists no predecessor in the given problem domain. For leaf nodes, we regard the predicates given as preconditions of the last applied operator. Thus, we obtain {*(ct (topof(topof C))), (on (topof(topof(C)) (topof C)))*} as relevant

[5]In some planning systems, cf. PRODIGY [VCP+95], the planner can be provided with additional information about control rules to make planning more efficient for a given domain; similarly, such systems could be extended to provide information about the data structures of the planning domain

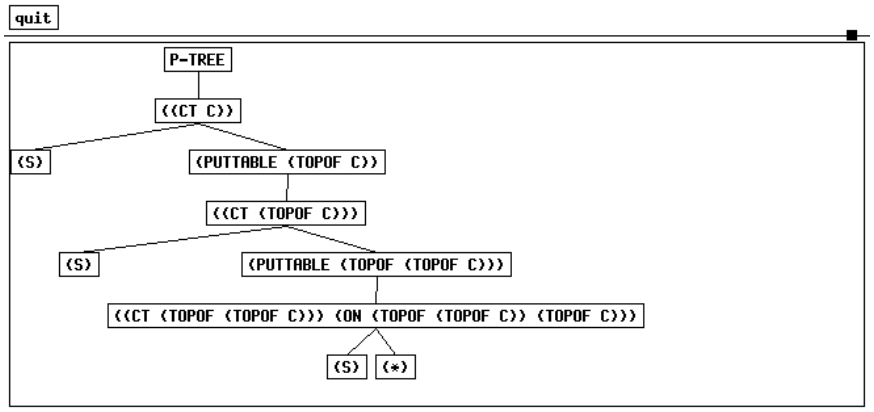

Figure 2.3: Binary planning tree for *clearblock*

predicates for the last state. But the predicate (*on (topof(topof(C)) (topof C))*) is not really relevant. It plays no role with regard to a general strategy for clearing a block. There are at least two heuristic strategies for cleaning the leaf-node predicates: First, we could assume that, if up to now only the operator *puttable* was used in planning this would also be the case for larger domains and accordingly replace the leaf-predicates by the instantiated add-list of *puttable*. Secondly, we could compare the predicates used so far and only accept such predicates of the preconditions which are also used in the predecessor nodes. But we will see, that we don't need such heuristics because the leaf states are not regarded when rewriting the plan to an initial program.

2.5 Generating a binary tree

Now we are nearly done with transforming the original plan tree into a format suitable for generalization. We will rewrite the plan into a binary structure. This structure is similar to the plans proposed by [Wys87] and can be used to generate the shortest operator sequences for a set of input states (similar to a universal plan) by means of an interpreter function.

In case of linear plans, generating the binary tree is simple: For each predicate node we introduce a left successor s. The variable s represents that there is a situation where the predicate given at the predecessor node is true, or, more generally, that the predicates given on the path to this leaf are true. The right successor is the next predicate node with the arc labelled by the operation. If there is no further operation given, we terminate the path with an asterix, indicating, that for this case, we have made no experience during planning. The binary tree for the *clearblock*-problem is given in figure 2.3.

In case of non-linear plans, we try to unify different paths. If there still remain non-linear subplans, we can either introduce an arbitrary sequence or we can try to generalize over the tree structure (see chapter 4). Note, that for unifying different paths of a plan it is crucial that constructive rewriting has been performed *before*: when searching a generalization, it is meaningless to unify sub-paths involving identical constants. But it makes sense to unify sub-paths with identical constructive expressions!

2.6 Transforming the plan tree into a program term

In the last step, we rewrite the final plan into the syntax required for initial programs as input in our program synthesis system[6]. To obtain the initial program for *clearblock* given above

$$\begin{aligned} G \quad = \quad & g(cleartop(x), s, puttable(topof(x), \\ & \qquad g(cleartop(topof(x)), s, puttable(topof(topof(x)), \\ & \qquad\quad \Omega)))) \end{aligned}$$

we regard each predicate node as a boolean condition, its left successor as the then-case and its right successor as else-case. Remember that the remaining constants in the rewritten plan are regarded as variables.

The plan can be read as "If block C is clear then return the current situation, else, put the top of block C on the table in a situation for which holds: if the top of block C is clear then ...". So, if an arc is labelled with an operator, the subtree becomes the additional argument of the operator. If the subtree of an operator does not contain a further operator and if there is an asterix in this subtree, we terminate rewriting by introducing Ω (representing the undefined sequence in infinite term algebras [Wys83, CN78]) as argument.

Note, that the *clearblock* macro not only generalizes over the size of the blocksworld domain (number of blocks), but also over the position of the block which is to be cleared. In planning, we made experience with clearing the bottom block of a tower, the recursive program can clear an arbitrary block x.

[6]Remark: this step is not implemented yet. Currently, we provide "handmade" initial programs or unfoldings of given recursive programs to our synthesis system. Eckhard Wiederhold now is implementing the transformation as part of a student project.

Chapter 3

DPlan – A non-linear backward planning system

It may be a little unusual to propose a non-linear backward planning algorithm when the state of the art is graphplan [BF97, KNH97] and SAT-planning [KS98]. But from the perspective of *learning in planning* efficient *plan construction* is not the crucial question. From a cognitive point of view [SWar], we want to model how experience with a small finite problem domain can be generalized to an efficient solution strategy for a complete class of problems. It is not very plausible to assume that a human problem solver would explore a logistics problem with 105 action steps [Wel99b]. In fact, we can assume that it is possible to extract a general strategy as "try to load as many objects as possible in the transporter if at one place" from very small problem domains (see transportation example in section 4). From the perspective of planning we propose an automated approach to generalize finite plans into cyclic macro-operations. For our synthesis method described shortly in section 2.1, initial programs which correspond to the third expansion of a hypothetical recursive program scheme are sufficient [MS98]. So, usually it is enough to construct plans in domains consisting of three objects of each given type and clearly for domains of such restricted size efficiency questions are not crucial. On the other hand, we argue that the availability of iterative macro-operations can lead to high efficiency gains when the planner is confronted with a problem of a already known class (cf. Tower of Hanoi with 3 discs or sorting of lists with three elements) but of different size (n discs, n list elements). Similar arguments as well as calculations of efficiency gains and experimental data for iterative macro-operators are presented by Shell and Carbonell [SC89].

In this chapter we will present the planning system DPlan and discuss it in relation to current planning methodology. The transformation of the originally output of DPlan into an initial program is discussed in chapter 4.

3.1 Semantics for D-plans

The planner DPlan produces the minimal spanning tree of a finite problem space (for an overview of algorithms for calculating spanning trees of graphs see cf. [GH85, Tar83]). The root of the spanning tree are all problem states which are subsumed by the planning goal. The minimal spanning tree represents the shortest operation sequences for all problem states. Because this idea is similar to the Dijkstra-algorithm for calculating all shortest paths to a given node in a graph [Dij59], we call our planner DPlan and the resulting plans D-plans.

3.1.1 Operators, states, and operations

First, we define operators, states, operations (actions)[1].

Definition 1 (Operator). An operator is a 4-tuple consisting of

1. a **name**, which is a string,
2. the **precondition** φ_0, which is a conjunction of positive literals,
3. an **operation template** $\hat{o}$, which is the operator name and the number and names of its arguments,
4. the **effect** $\varphi \Rightarrow \alpha_t\delta_t, \alpha_e\delta_e$, with φ as effect condition (limited to a single positive literal), α_t and δ_t as Add- rsp. Del-effects if φ is true and α_e and δ_e as Add- rsp. Del-effects if φ is false.
   When $\varphi = \emptyset$, $\alpha_t = \emptyset$, $\delta_t = \emptyset$, the operator is "unconditioned".

Note, that in contrast to the definition of operators in other planning systems [PW92, VCP+95, KNH97] we do not give an explicit parameter list. Up to now, we have only untyped parameters. Furthermore, we currently allow only a single effect condition. In the current (first) implementation of the planner we do not regard negation, disjunction and universal quantifiers. We hope to extend our approach to these features.

An example of the valid operator *put* is given in figure 3.1. This operator has a conditional effect: A block x can be put on a block y if both blocks are clear. Applying this operation has the (primary) effects [FY95] that x lies on y and y is no longer free. If block x was on the table, these are the only effects; but, if x was lying on another block z, there are the additional (side-) effects that x does no longer lay on z and z is clear. Figure 4 shows only *one* possibility for representing the *put*-operator. Another possibility is, to introduce an additional predicate $ontable(x)$.

Definition 2 (State). A **state** is a set of positive ground literals (ground atoms). We denote a positive ground literal with p and the set of positive ground literals with P. As usual, a state $S \in 2^P$ is a set of ground atoms.

Examples for states are $\{on(A,B), on(B,C), ct(A)\}$, $\{ct(A), ct(B), ct(C)\}$.

[1] Terminology and sequence of our definitions follow mostly [KNH97].

name: put

pre : {cleartop(x), cleartop(y)}

op : put(x,y)

eff : if on(x,z)
then ADD {ct(z), on(x,y)} DEL {on(x,z), ct(y)}
else ADD {on(x,y)} DEL {ct(y)}

Figure 3.1: The operator *put*

name: put

pre : {cleartop(x), cleartop(y), for-all(z) ¬ on(x,z)}

op : put(x,y)

eff : ADD {on(x,y)} DEL {ct(y)}

name: put

pre : {cleartop(x), cleartop(y), on(x,z)}

op : put(x,y)

eff : ADD {ct(z), on(x,y)} DEL {on(x,z), ct(y)}

Figure 3.2: The two variants of the *put*-operator

Definition 3 (Operation/Variants of an operator). An **operation** o is a ground instance of an operator. That is, all parameters are instantiated with constants. Additionally, operations have unique effects. A conditioned operator (as defined in def. 1) results in two operations: $\varphi_0 \cup \varphi; \alpha_t, \delta_t$ and $\varphi_0 \cup \neg\varphi; \alpha_e, \delta_e$. We call the uninstantiated forms of a conditioned operator **variants**.

We use the term operation instead of the often used term *action*. In contrast to other planners dealing with operators with conditioned effects, in DPlan each operation has a unique effect. Instantiation of a conditioned operator is performed with respect to its variants. The variants of the *put*-operator are given in figure 3.2. Note, that while DPlan does currently not allow for negation and universal quantifiers in specifying *operators* we can deal with such expressions in operator *variants*[2]. To apply α_e, δ_e we have to make sure that $\neg\varphi$ holds *for all* possible instantiations.

An example of two *put*-operations derived by instantiating the *put*-operator

[2] Of course, we have not really to deal with universal quantifiers because $\forall x \neg p(x) \equiv \neg\exists p(x)$.

o_1 is valid for $\{ct(A), ct(B), ct(C)\}$ in a blocksworld of three blocks A, B, C

name: put

pre : {cleartop(B), cleartop(C), ¬ on(B,A)}

op : put(B,C)

eff : ADD {on(B,C)} DEL {ct(C)}

---

o_2 is valid for $\{on(B,A), ct(B), ct(C)\}$ in a blocksworld of three blocks A, B, C

name: put

pre : {cleartop(B), cleartop(C), on(B,A)}

op : put(B,C)

eff : ADD {ct(A), on(B,C)} DEL {on(B,A), ct(C)}

Figure 3.3: Operations derived from the *put*-operator

is given in figure 3.3. In a blocksworld consisting of only three blocks A, B, and C, the preconditions φ_0 constrain the possible instantiations of z. For the examples in figure 3.3 we have $\sigma = \{z \leftarrow A\}$.

3.1.2 Operation applications, admissible states, and variable bindings

In the following we will define the application of a single operation and of operation sequences. Furthermore, we will describe how the admissibility of calculated predecessor states is checked, when operations are applied backward and we will describe how variable bindings are determined in backward-planning. Remember, that our planner works for *sets of initial states*, i.e. it calculates a cpo for all states of a finite problem domain.

Definition 4 (Applying an operation). We denote the set of all operators with $\mathcal{O}$. Let O be the set of all ground instances (operations) of $\mathcal{O}$ and $Res : 2^P \times O \to 2^P$ be a function from states and operations to states.
Application of a single operation o to a state S is defined as

$$Res(S,o) = \begin{cases} (S \cup A(S,o)) \setminus D(S,o) & ; if\ \varphi_o \subseteq S \\ undefined & ; otherwise \end{cases}$$

with $A(S,o)$ as instantiated ADD-list of o and $D(S,o)$ as instantiated DEL-list.

Definition 5 (Applying an operation-sequence). The function Res can be expanded to $\overline{Res} : 2^P \times O^* \to 2^P$ with O^* as set of operation-sequences. Application of a sequence of operations can be defined recursively as

$$\overline{Res}(S, < o_1, \ldots o_n >) = \begin{cases} Res(S, o) & ; \; if \; \| < o_1 \ldots o_n > \| = 1 \\ Res(\overline{Res}(S, < o_1, \ldots o_{n-1} >), o_n); & otherwise. \end{cases}$$

The forward application of operation-sequences is used in *plan execution*.

Because our planner is working *backward*, starting from the goal, we also have to define backward application of an operation and backward application of a set of operations.

Definition 6 (Applying an operation backward). We denote the set of all operators with $\mathcal{O}$. Let O be the set of all ground instances (operations) of $\mathcal{O}$ and $Res : 2^P \times O \to 2^P$ be a function from states and operations to states. Application of a single operation o **backward** to a state S is defined as

$$Res^{-1}(S, o) = \begin{cases} (S \cup (D(S, o) \cup \varphi_0)) \setminus A(S, o) & ; \; if \; A(S, o) \subseteq S \\ undefined & ; \; otherwise \end{cases}$$

with $A(S, o)$ as instantiated ADD-list of o and $D(S, o)$ as instantiated DEL-list.

In contrast to forward-planning, we have to check whether a predecessor state S' calculated by $Res^{-1}(S, o)$ is *admissible*, i.e. if it is a legal state in the planning domain. In our context, a state is admissible if it is consistent. For example, a state in which block A lies on block B and block B is *cleartop* is not admissible because it contains contradictory propositions.

Admissibility can be checked via axioms (see [Wys87] and model-based planning [CRT98]). That is, $S = \{p_1 \ldots p_n\} \in 2^P$ is admissible if there is no axiom where from some p_i's $\in S$ follows $\neg p_j$ and $p_j \in S$. Another possibility to check admissibility is, to generate a candidate predecessor S' by backward application of operation o and check whether $Res(S', o) = S$ for the current state S.

We can exploit the fact that our planner works on sets of initial states (i.e. all states of a given finite problem domain). Because all states of a problem space and no other states are admissible for a giving planning problem we can omit the introduction of axioms and defined instead

Definition 7 (Admissibility of states). For a given planning problem in problem space $\mathcal{D}$ a state S is **admissible** iff $S \in \mathcal{D}$.

We illustrate the calculation of admissible predecessor states by backward application of an operation, again with the familiar blocksworld example (see figure 3.4). For the clarity of the example we assume that only the *put*-operator given in figure 3.1 is available (for a complete specification of the *tower*-problem we have to provide additionally a *puttable*-operator, see section 2.2 and below). We obtain four instantiated operators consistent with the current state S. For the three-block world, variable z can only be instantiated with C for put_1 and put_2 and only with A for put_3 and put_4. Only the application of put_1 leads to an admissible predecessor state. For put_2 subtraction of the Add-list from S can not be performed completely, because $ct(C) \notin S$ and there is no state in $\mathcal{D}$

Set of all admissible states of the three-block blocksworld:
$\mathcal{D}_{tower}$ = { {on(A B), on(B C), ct(A)}, {on(B C), ct(A), ct(B)}, {ct(A), ct(B), ct(C)}, {on(B A), on(A C), ct(B)}, {on(C A), on(A B), ct(C)}, {on(A C), on(C B), ct(A)}, {on(B C), on(C A), ct(B)}, {on(C B), on(B A), ct(C)}, {on(A B), ct(A), ct(C)}, {on(A C), ct(A), ct(B)}, {on(B A), ct(B), ct(C)}, {on(C A), ct(B), ct(C)}, {on(C B), ct(A), ct(C)}, }

Current state: $S = \{on(A,B), on(B,C), ct(A)\}$

Put-operations:

name: put_1

pre : {cleartop(A), cleartop(B), ¬ on(A,C)}

op : put(A,B)

eff : ADD {on(A,B)} DEL {ct(B)}

name: put_2

pre : {cleartop(A), cleartop(B), on(A,C)}

op : put(A,B)

eff : ADD {ct(C), on(A,B)} DEL {on(A,C), ct(B)}

name: put_3

pre : {cleartop(B), cleartop(C), ¬ on(B,A)}

op : put(B,C)

eff : ADD {on(B,C)} DEL {ct(C)}

name: put_4

pre : {cleartop(B), cleartop(C), on(B,A)}

op : put(B,C)

eff : ADD {ct(A), on(B,C)} DEL {on(B,A), ct(C)}.

Calculating predecessors:

$$\begin{aligned}
Res^{-1}(S, put_1) &= \{on(B,C), ct(A), ct(B)\}\surd \\
Res^{-1}(S, put_2) &= \{on(B,C), ct(A), on(A,C), ct(B)\} * \\
Res^{-1}(S, put_3) &= \{on(A,B), ct(A), ct(B), ct(C)\} * \\
Res^{-1}(S, put_4) &= \{on(A,B), ct(B), on(B,A), ct(C)\} * .
\end{aligned}$$

Figure 3.4: Calculating predecessor states and selecting admissible predecessors

where two blocks are lying on C simultaneously. There are also no state in $\mathcal{D}$ where A lies on B and B is cleartop (or where A lies on B and B simultaneously on A).

The crucial (and most time-consuming) aspect in backward-planning is to calculate the variable bindings [BW94] which are used to instantiate operators and thereby to generate predecessor states. As for determining admissible states, we can again rely on the predefined set of states $\mathcal{D}$.

Definition 8 (Determination of variable bindings). Let S be the current state. For each variant $\hat{o}$ of an operator with precondition φ_0 (which can either be $\varphi_0 \cup \varphi$ or $\varphi_0 \cup \neg\varphi$) and effect $\alpha\delta$ (which can either be $\alpha_t\delta_t$ or $\alpha_e\delta_e$) we determine the set of legal variable bindings in the following way:

1. $\Sigma = \emptyset$
2. Find all substitutions σ_i with $\alpha_\sigma \subseteq S$.
   Set Σ to $\Sigma = \{\sigma_i \mid i = 1 \ldots n\}$.
3. For each σ_i find all compatible substitutions ρ with $\varphi_{0_{\sigma_i\rho}} \subseteq S'$ with $S' \in \mathcal{D}$.
   Set each $\sigma_i \in \Sigma$ to $\sigma_i\rho$.

The composition of substitutions σ and ρ is compatible iff for $(v \leftarrow t) \in \sigma$ there exists no $(v' \leftarrow t') \in \rho$ with $v = v'$ and $t \neq t'$, where t, t' are constants.

We will show below that by using look-ups in the set of states $\mathcal{D}$ determining variable bindings can be performed much more efficient than in usual backward-planning approaches.

Finally, we can define the application of "parallel operations". That is, we describe how the complete set of legal predecessors of a state is calculated.

Definition 9 (Applying a set of operations backward). Let Res_p^{-1} be a function from states and sets of operations to sets of states $Res_p^{-1} : 2^P \times 2^O \rightarrow 2^{2^P}$ and $\mathcal{D}$ the set of all states of a given finite problem domain.

The backward application of a set of operations $\{o_1 \ldots o_n\}$ results in a set of admissible predecessor states $\{S_1 \ldots S_m\}$ with $m \leq n$ and is defined in the following way:

$$Res_p^{-1}(S, \{o\}) = Res^{-1}(S, o)$$

$$Res_p^{-1}(S, \{o_i \ldots o_n\}) = \begin{cases} \{Res_p^{-1}(S, o_1)\} \cup Res_p^{-1}(S, \{o_{i+1} \ldots o_n\}) & \\ \quad with\ i = 1 \ldots n & ;\ if\ Res_p^{-1}(S, o1) \in \mathcal{D} \\ Res_p^{-1}(S, \{o_{i+1} \ldots o_n\})\ with\ i = 1 \ldots n; & otherwise. \end{cases}$$

For the state $\{$*on(B,C), ct(B), ct(A)*$\}$ the application of a set of *put* operations results in the admissible predecessors $\{$ $\{$*ct(A), ct(B), ct(C)*$\}$, $\{$*on(B,A), ct(B), ct(C)*$\}$$\}$. Of course, when planning in the blocksworld domain we also regard applications of *puttable*-operations.

3.1.3 Implementation details

In our implementation, operators are defined in the following way:

```
(setq rules '(
  (rule puttable
    (if ( ct (> x) ) ( on (< x) (> y) ) )
    (then (puttable (< x)))
    (conq ( add ((ct (< y)))
            del ((on (< x) (< y))) ) )
  )
  (rule put
     (if ( ct (> x) ) ( ct (> y) ) )
     (then (put (< x) (< y) ) )
     (conq (cond ((on (< x) (> z))
                  (add ((on (< x) (< y)) (ct (< z)))
                   del ((ct (< y)) (on (< x) (< z)))))
               ( T ( add ((on (< x) (< y)))
                     del ((ct (< y))) ) ) ) ) )
))
```

The representation of variables as `(> x)` or `(< y)` is different from the often used form `?x`. We were inspired by Winston's definition of match-select-apply cycles for production systems [WH89]. The symbol $>$ indicates that a variable has to be instantiated in accordance to the current state; the symbol $<$ that a variable has be instantiated in accordance with the current set of bindings. For backward-application of operations the semantics of $<$ rsp. $>$ is switched. In the next implementation we want to replace $>$ / $<$ by the usual ? notation. This can be realized with only a slight modification in the algorithm: If there is a variable: first look in the list of current bindings, if the variable is bound, take this value, otherwise, obtain the binding from the current state.

We already remarked above that we hope to expand our first implementation of DPlan to full ADL-syntax [Ped89]. Furthermore, we want to introduce the possibility of defining data types for variables and we want to allow for external function calls in operations similar to PRODIGY [BCE+92]. These extensions are crucial if we want to use our planner for calculating initial programs for list and number problems which are usually regarded in program synthesis (see chapter 4 and appendix E).

3.2 The algorithm DPlan

After defining the backward application of sets of operations to a current state thereby generating the set of all admissible predecessor states we are ready to define the planning problem.

Definition 10 (Planning problem). **A planning problem** $\mathcal{P}(\mathcal{O}, \mathcal{D}, \mathcal{G})$ is a 3-tuple where $\mathcal{O}$ is the set of operators, $\mathcal{D}$ a set of initial states and G a set of planning goals. All states $S \in \mathcal{D}$ and G are sets of positive ground literals. In the context of our work, $\mathcal{D}$ is the set of all states of a finite problem domain.

Planning goals are currently restricted to conjunctions of (possibly interdependent) subgoals (cf. $\{on(A,B),\ on(B,C)\}$). We are planning to extend goal definitions to negation, disjunction and universal quantification in the future.

Let C be the set of all constants (i.e. arguments of predicates $P \in S$) occurring in $\mathcal{D}$. The set of operations O is the set of all possible ground instances of $\mathcal{O}$ with respect to C.

Definition 11 (Minimal spanning tree of $\mathcal{P}$). [3] The minimal spanning tree of a planning problem $\Theta = (V, E)$ with the set of nodes V, the set of edges $E \subseteq V \times V$, and two labeling functions $\beta_V : V \rightarrow \mathcal{D}$, $\beta_E : E \rightarrow O$ is a tree with arbitrary branching factor, i.e.

$$\Theta = \theta \begin{cases} \lambda & (empty\ tree) \\ v \in V & (leaf) \\ v(\theta_1 \ldots \theta_b) & with\ 0 \leq b \leq ||\mathcal{D}|| \end{cases}$$

for which the following conditions hold:
(With Θ_l we denote the tree from the root to level l and with N_l we denote all nodes in Θ_l.)

1. The root $\Theta(N, E)_0$ is
   - $\perp$ if there is no state $S \in \mathcal{D}$ for which $G \subseteq S$ holds (the planning problem is not solvable), or
   - $v\ with\ \beta_V(v) = S$ if $S \in \mathcal{D}$ and $G \subseteq S$, and if there is no other state $S' \in \mathcal{D}$ with $G \subseteq S'$ (there is exactly on state which fulfills the goal), or
   - (root node and first level of Θ) $v(w_1 \ldots w_n)$ with $\beta_V(v) = G$ and $\beta(w_i) = S$ if $S \in \mathcal{D}$ and $G \subseteq S$ and $\beta_E(v, w_i) = \epsilon$ for all w_i, $2 \leq i \leq ||\mathcal{D}||$ (there is more than one state fulfilling the goal).
2. For each leaf node v of Θ_l the children are all $w \in Res_p^{-1}(\beta_V(v), O)$ with w is admissible (i.e. $w \in \mathcal{D}$) and $w \notin N_l$. For each edge (v, w) we have the label $\beta_E(v, w) = o$ with $Res^{-1}(\beta_V(v), o) = w$.

For the formal background and a variety of algorithms for spanning trees, see cf. [GH85, Tar83].

Definition 11 entails the planning algorithm (which we already described informally in chapter 2) given in table 3.1[4]. We will discuss its soundness,

[3] Remark: Def. 11 and the presentation of the algorithm are a bit overloaded. I will formulate both "more elegantly" soon.

[4] The initialization and the tree expansion are not easy to read, because I discriminate between nodes v, w and node labels $\beta_V(v)$ which are corresponding to states. When simplifying def. 11 the algorithm can be simplified accordingly. Note, that the planning algorithm can be implemented more efficient when integrating the calculation of variable bindings and state candidates in one loop, see efficiency of DPlan below.

completeness and optimality in section 3.4 and we will give further examples below and in section 4. Information about the current implementation of DPlan is given in appendix A.

Note, that in the implementation of DPlan we generate a graphical output of Θ where the operations are not given as edge labels but as intermediate nodes between nodes representing problem states.

Illustration: a blocksworld example

To generate a plan for building a tower of blocks A, B, C in a three-block world, we present DPlan with the following problem specification:

```
(setq states '(
 ((on a b) (on b c) (ct a))
 ((on b c) (ct a) (ct b))
 ((ct a) (ct b) (ct c))
 ((on b a) (on a c) (ct b))
 ((on c a) (on a b) (ct c))
 ((on a c) (on c b) (ct a))
 ((on b c) (on c a) (ct b))
 ((on c b) (on b a) (ct c))
 ((on a b) (ct a) (ct c))
 ((on a c) (ct a) (ct b))
 ((on b a) (ct b) (ct c))
 ((on c a) (ct b) (ct c))
 ((on c b) (ct a) (ct c))
))

(setq goal
 '( (on a b) (on b c) )
)
```

The ordering of states and of goal propositions is arbitrary. The operator definitions for `put` and `puttable` were already presented in section 3.1.3.

Planning starts with checking whether there are states fulfilling the `goal`: state `((on a b) (on b c) (ct a))` fulfills the goal and no other state, therefore it gets the root of the plan and is deleted from the list of current states (see "initialization" in table 3.1).

In the next step (first node expansion, see table 3.1), DPlan generates all such instantiations of all operator variants which ADD-list literals match with a literal of the current state. That is, candidates are `(puttable x)` with `y = A` realizing `(ct A)`, `(put A B)` with and without `(on A z)` realizing `(on A B)` and `(put B C)` with and without `(on B z)` realizing `(on B C)`. Each operation candidate is applied backwards (adding literals from the DEL-list and preconditions and deleting literals from the ADD-list from the current state). The calculation of state candidates, selection of admissible candidates, and determination of variable bindings was already illustrated in section 3.1.2. In figure 3.4 we demonstrated for the `put` operator, that only one of the four candidates results in

Table 3.1: The planning algorithm DPlan

input : a planning problem $\mathcal{P}(\mathcal{O}, \mathcal{D}, G)$

output : the minimal spanning tree Θ for $\mathcal{P}$ or $\perp$

initialization: $\Delta = \mathcal{D}$, the set of current states

$$\Theta := \begin{cases} (1)\perp;\ if\ there\ exists\ no\ state\ S \in \mathcal{D}\ with\ G \subseteq S \\ (2)v\ with\ \beta_V(v) = S \in \mathcal{D}\ and\ G \subseteq S;\ if\ there\ exists\ no\ other\ state \\ \qquad\qquad\qquad\qquad\qquad\qquad\qquad\qquad S' \in \mathcal{D}\ with\ G \subseteq S' \\ (3)v(w_1 \ldots w_n)\ with\ \beta_V(v) = G\ and\ \beta_V(w_i) = S_i\ \ \forall S_i \in \mathcal{D}\ and\ G \subseteq S_i, \\ with \beta_E(G, S_i) = \epsilon, i = 1 \ldots n, 2 \leq n \leq \|\mathcal{D}\|;\ otherwise \end{cases}$$

for (2) and (3) Δ is reduced: $\Delta := \Delta\ \{\beta_V(v)\}$ for (2) and $\Delta := \Delta\ \{\beta_V(w_i)\} \forall w_i$ for 93).

- **Terminal condition:** $\Delta = \emptyset$ (success) or $\neg\exists v$ for which def. 11.3 holds (failure)
- **Tree expansion:** (1)
  If $\Theta = \perp$
  Then return bottom
  Else For all leafs v_i of $\Theta = \theta(v_1 \ldots v_n)$ do
  1. Calculate $\Omega = \{w | \beta_V(w) \in Res_p^{-1}(\beta_V(v), O) \in \Delta\}$
  2. $\Delta := \Delta \setminus \{\beta_V(w)\} for all w \in \Omega$
  3. Expand Θ:
     Replace leaf v_i by $v_i(w_1 \ldots w_m)$ with $\beta_E(v_i, w_j) = o$ with $Res_p^{-1}(\beta_V(v_i), O) = \{w_1 \ldots w_m\}$.

  endfor
- **Node expansion:** (2)
  For the current state $S = \beta_V(v)$ and for each variant $\hat{o}$ of an operator in $\mathcal{O}$ with $\hat{o} = \varphi_0, \alpha\delta$
  - Determination of all sets of legal bindings (2a)
    1. $\Sigma = \emptyset$
    2. Find all substitutions σ_i with $\alpha_\sigma \subseteq S$
    3. $\Sigma := \{\sigma_i \mid i = 1 \ldots k\}$
    4. For each σ_i find all compatible substitutions ρ with $\varphi_{0_{\sigma_i \rho}} \subseteq S'$ with $S' \in \mathcal{D}$.
    5. Set each $\sigma_i \in \Sigma$ to $\sigma_i := \sigma_i \rho$.
  - Construct all operation candidates $O = \{o \mid \hat{o}_{\sigma_i}\}$
  - Calculate all state candidates

    $$\mathcal{C} = Res^{-1}(S, o) = \begin{cases} (S \cup (D(S, o) \cup \varphi_0)) \setminus A(S, o) & ;\ if\ A(S, o) \subseteq S \\ undefined\ and\ O := O \setminus \{o\} & ;\ otherwise \end{cases}$$

    with $A(S, o)$ as instantiated ADD-list of o and $D(S, o)$ as instantiated DEL-list.
  - **For all** $c \in \mathcal{C}$ with $c \notin \Delta$ do $\mathcal{C} := \mathcal{C} \setminus c$
  - Return all pairs (o, S') with $S' = Res^{-1}(S, o) \in \mathcal{C}$